

PTO/SB/08a/b (08-03)

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Substitute for form 1449A/B/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet

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of

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Complete If Known

Application Number	09/192,205-Conf. #9482
Filing Date	November 13, 1998
First Named Inventor	Gail A. Alverson
Art Unit	2127
Examiner Name	K. Tang

Attorney Docket Number 324758001US

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
MA		US-6,560,626-B1	05-06-2003	Hogle et al.	
MA		US-6,584,489-B1	06-24-2003	Jones et al.	

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		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)				

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Examiner Signature

Majid Banoubali

Date Considered

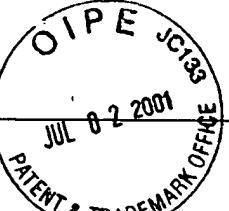
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COMPLETE IF KNOWN

Application Number	09/192,205
Confirmation Number	9482
Filing Date	November 13, 1998
First Named Inventor	Gail A. Alverson
Group Art Unit	2756
Examiner Name	

Sheet 1 of 4 Attorney Docket No. 324758001US

U.S. PATENT DOCUMENTS

*EXAMINER INITIALS	Cite No.	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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		Office	Number				
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	AN						

OTHER PRIOR ART-NON PATENT LITERATURE DOCUMENTS

*EXAMINER INITIALS	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume/issue number(s), publisher, city and/or country where published.	T
MJ	AO	Gail Alverson et al., "Tera Hardware-Software Cooperation," Proceedings of Supercomputing 1997, San Jose, California, November 1997.	
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Mary B. Montebello

DATE CONSIDERED

9/14/04

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<p style="text-align: center;">INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i></p> <p style="text-align: right;">JUL 02 2001</p> <p style="text-align: left;">O I P E P A T E N T & T R A D E M A R K O F F I C E</p>				COMPLETE IF KNOWN	
				Application Number	09/192,205
				Confirmation Number	9482
				Filing Date	November 13, 1998
				First Named Inventor	Gail A. Alverson
				Group Art Unit	2756
Examiner Name					
2	of	4	Attorney Docket No.	324758001US	

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MJ	AP	Jenn-Yuan Tsai et al., "Performance Study of a Concurrent Multithreaded Processor," IEEE, 1998. pp. 24-35.	

EXAMINER*Majid Barankhali***DATE CONSIDERED***9/14/04*

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<p style="text-align: center;">Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)</p> <p style="text-align: right;">JUL 02 2001 O I P E JC133 PATENT & TRADEMARK OFFICE</p>				COMPLETE IF KNOWN	
				Application Number	09/192,205
				Confirmation Number	9482
				Filing Date	November 13, 1998
				First Named Inventor	Gail A. Alverson
				Group Art Unit	2756
				Examiner Name	
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MB	AM	Priyadarshan Kolte and Mary Jean Harrold, "Load/Store Range Analysis for Global Register Allocation," ACM-SIGPLAN, June 1993.	
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MD	AP	Preston Briggs et al, "Coloring Heuristics for Register Allocation," Department of Computer Science, Rice University, Houston, Texas, June 1989.	

EXAMINER	<i>Maryal Boen</i>	DATE CONSIDERED
		<i>9/14/04</i>

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<p style="text-align: right;">O P E R A T I O N S JUL 02 2001 PATENT & TRADEMARK OFFICE</p>		Application Number 09/192,205 Confirmation Number 9482 Filing Date November 13, 1998 First Named Inventor Gail A. Alverson Group Art Unit 2756 Examiner Name		 JUL 3 2001	
4	of	4	Attorney Docket No.	324758001US	

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	AK							
	AL							
	AM							
	AN							
	AO							

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